

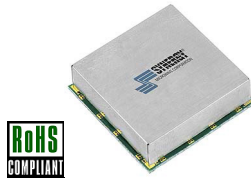
SELECTABLE FREQUENCY SYNTHESIZER

SURFACE MOUNT MODEL: FCPL-70

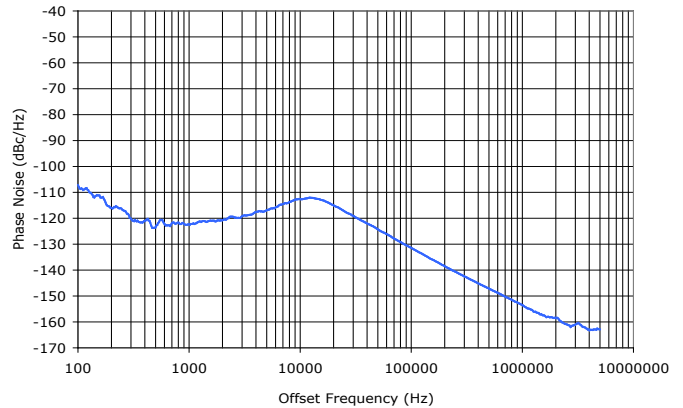
65 - 72 MHz

FEATURES:

- ▶ Exceptional Phase Noise
- ▶ No Programming Required
- ▶ Lead Free - RoHS Compliant
- ▶ Patented REL-PRO® Technology
- ▶ Small Size, Surface Mount



Phase Noise



SPECIFICATIONS (Rev. C 12/21/10)

Frequency	65 - 72 MHz		
Reference Input Frequency	10 MHz		
Reference Input Voltage	1.0 V p-p to 3.3 V p-p		
Bias Voltage	Vcc	V (Vdc)	I (mA Max.)
	Vcc 1 (Digital)	+5	15
	Vcc 2 (Vtune & VCO)	+5	45
D0, D1, D2	0 V / +5 V		
Output Power	+1 dBm (Min.)		
Spurious Suppression	80 dB (Typ.)		
Harmonic Suppression	12 dB (Typ.)		
Power Up Time	90 mSec (Typ.) ¹		
Output Impedance	50 Ohms (Nom.)		
Typical Phase Noise	Offset	Phase Noise	
	@ 1 kHz	-120 dBc/Hz	
	@ 10 kHz	-112 dBc/Hz	
	@ 100 kHz	-131 dBc/Hz	
Lock Detect Indicator	CMOS 3.3 V		
Operating Temperature Range	-40 to +85 °C		

¹ From stable power on to stable lock detect.

Binary Frequency Selection Table

D2*	D1*	D0*	Freq. (MHz)
0	0	0	65
0	0	1	66
0	1	0	67
0	1	1	68
1	0	0	69
1	1	1	70
1	0	1	71
1	1	0	72

* Voltage levels: 0V or gnd. / +5V or open.

Absolute Maximum Ratings

Storage Temp. Range	-55 to +125 °C
Bias Voltage (Digital)	+5.25 V
Bias Voltage (Vtune & VCO)	+5.5 V
DC Voltage Applied to RF Out	± 25 V

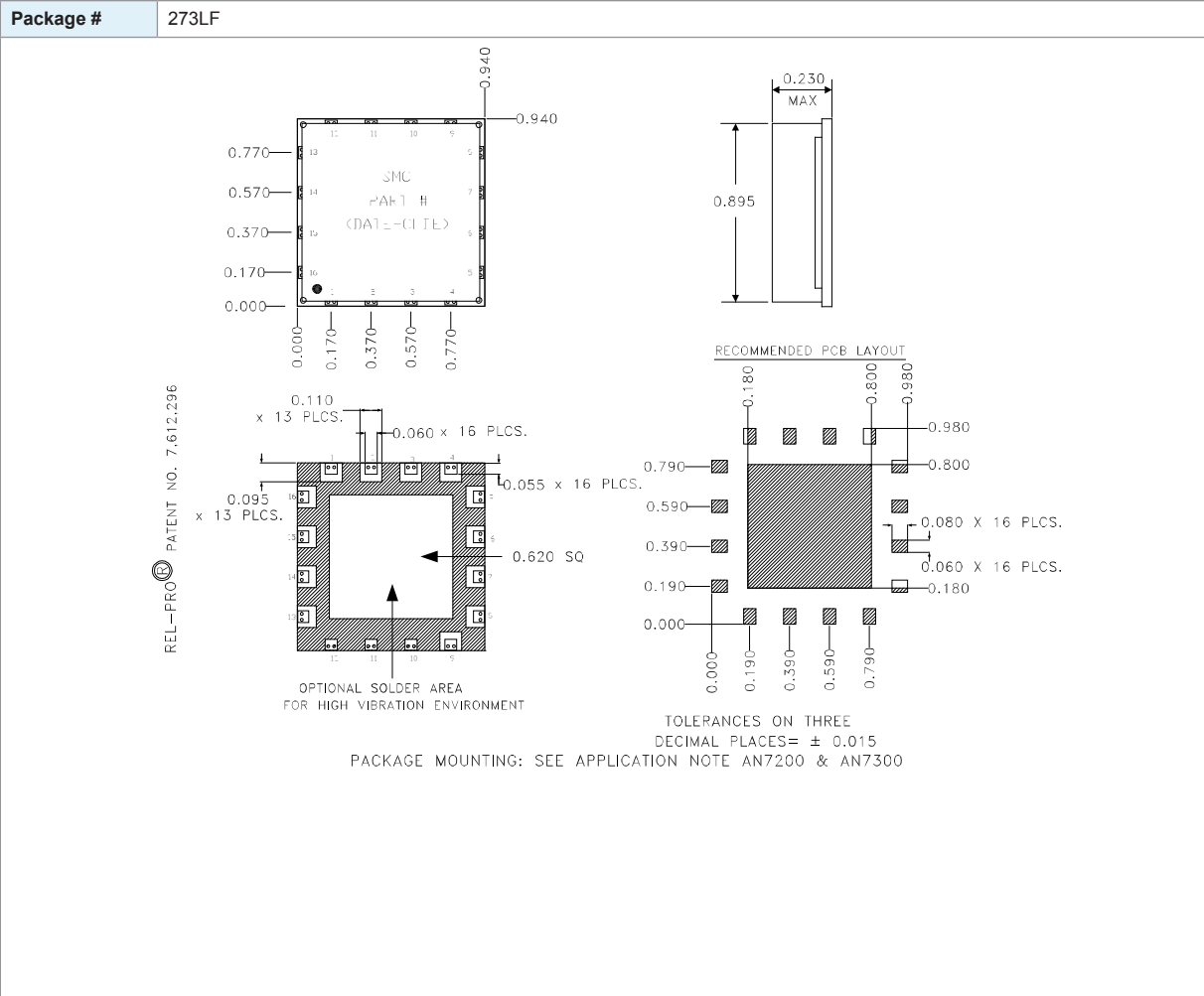
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Port Configuration

Pin 1 - NC	Pin 6 - Ref. Input	Pin 11 - Ground	Pin 16 - NC
Pin 2 - D0	Pin 7 - VCC1 (Digital)	Pin 12 - Ground	NC - Not Connected
Pin 3 - D1	Pin 8 - VCC2 (Vtune & VCO)	Pin 13 - RF Out	
Pin 4 - D2	Pin 9 - NC	Pin 14 - Lock Detect	
Pin 5 - NC	Pin 10 - Ground	Pin 15 - NC	

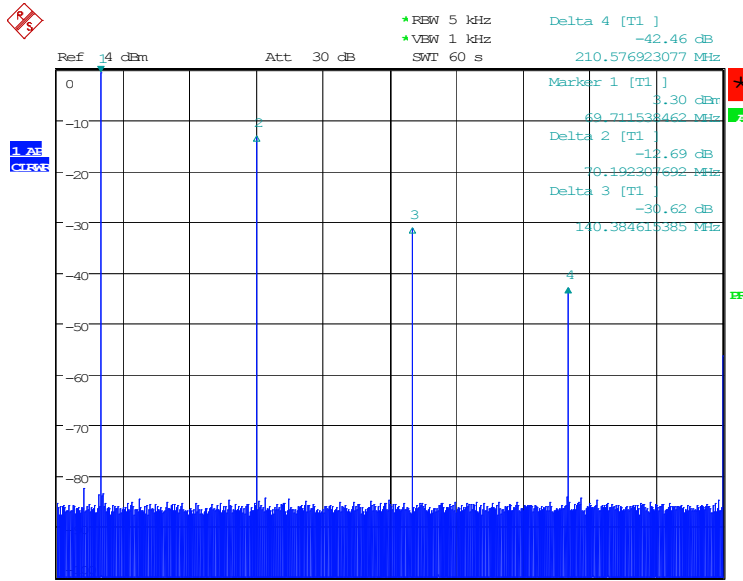
PERFORMANCE PLOTS

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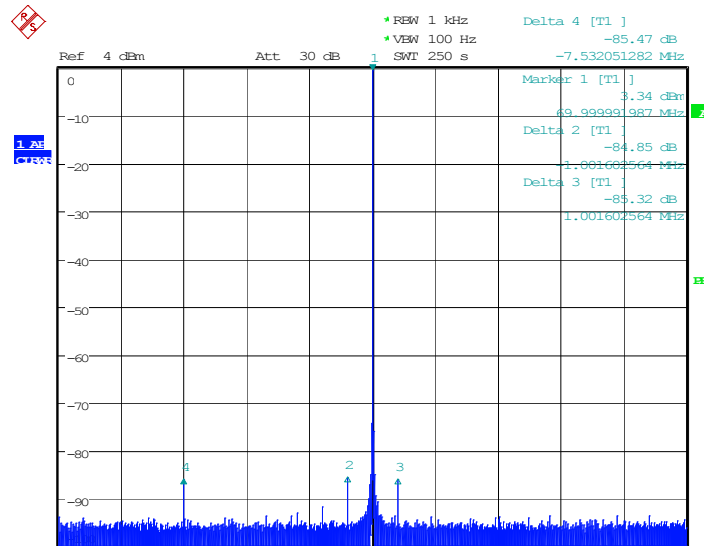
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HARMONIC SUPPRESSION



REFERENCE SIDEBAND SUPPRESSION

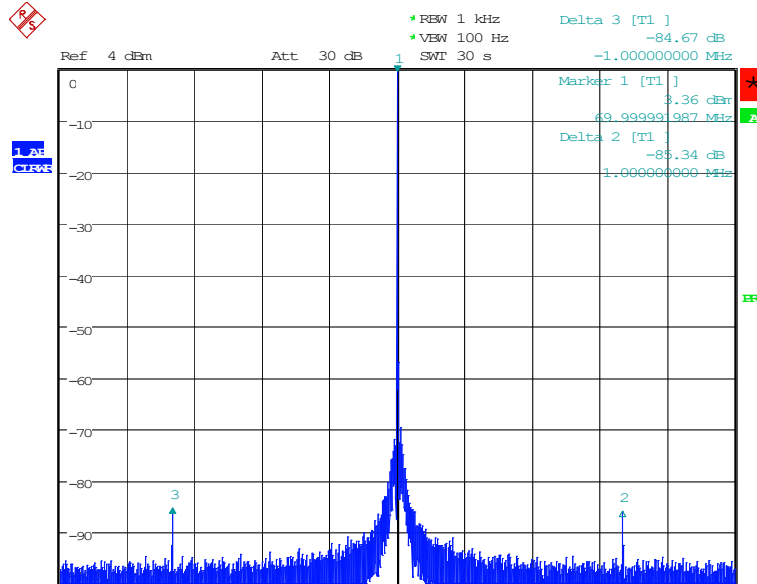


PERFORMANCE PLOTS

SELECTABLE FREQUENCY SYNTHESIZER SURFACE MOUNT MODEL: FCPL-70

65 - 72 MHz

CLOSE IN SPURIOUS SUPPRESSION



PERFORMANCE PLOTS